

# ABSTRACT OF THE DISCLOSURE

First and second semiconductor switches which are activated alternately are provided between ends of a primary winding and a common potential point, wherein a DC power supply voltage is supplied to a center tap. An electric current flowing into a load is fed back to thereby subject the semiconductor switches to PWM control. Series circuits consisting of capacitors and semiconductor switches are connected between the center tap of the primary winding and the ends of the same. The semiconductor switches are activated in synchronism with the first and second semiconductor switches, thereby preventing occurrence of an anomalous high voltage, which would otherwise be caused at the time of switching operation.

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